PTO/SB/30 (08-00)

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This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

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REQUEST

TINUED EXAMINATION (RCE) **TRANSMITTAL**

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995. See The American Inventors Protection Act of 1999 (AIPA).

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Application Number	09/843,820	\supset ,
Filing Date	April 30, 2001	#
First Named Inventor	Takahiko SATO	7
Group Art Unit	2816	005
Examiner Name	A. Luu	Part
Attorney Docket Number	108397-00042	150

		<u>NOTE:</u>	37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified applic to consider filing a continued prosecution application (CPA) under 37 C.F.R. § the patent term adjustment provisions of the AIPA. See Changes to application 65 Fed. Reg. 50092 (Aug. 16, 2000); Interim Rule, 65 Fed. Reg. 14865 (Mar. which established RCE practice.	§ 1.53(d) (PTO/SB. on Examination an	/29) instead of a RCE to be eligible fo d Provisions Application Practice, Fin	<i>r</i> e Rule,		
L				***	a:			
	1.	Submis a. 🗵 i. ii. jii. b. '' 🗵	Previously submitted Consider the amendment(s)/reply under 37 C.F. (Any unentered amendment(s) referred to above will be entered). Consider the arguments in the Appeal Brief or F. Other: Enclosed		reviously filed on July 10	EIVED 22 2002 Y CENTER		
		i. ii. iii. iv.	 ☑ Preliminary Amendment/Reply ☐ Affidavit(s)/Declaration(s) ☐ Information Disclosure Statement (IDS) ☑ Other: 			2800		
	2.	. (Miscellaneous)						
.•	3.	a. Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a period of months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17(i) required) b. Other: The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.						
}		a. The Director is hereby authorized to charge any deficiency in the following fees or credit any overpayments to Deposit Account No. 01-2300, referencing 108397-00042 i. RCE fee required under 37 C.F.R. § 1.17(e)						
		ii. 🛛 Extension of time fee (37 C.F.R. §§ 1.136 and 1.17)						
		iii. Other:						
		b. ⊠ Check No. <u>346/46</u> in the amount of \$ <u>1140.00</u> .						
ľ	SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED							
t	Nam	ne (Print/	10.		tion No. (Attorney/Agent)	46,412		
ŀ		nature)	Lines Alados as	Date	August 19, 2002	-		
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Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND Fees and Completed Forms to the following address: Assistant Commissioner for Patents, Box RCE, Washington, DC 20231.

CERTIFICATE OF MAILING OR TRANSMISSION

Date

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner For Patents, Box RCE, Washington, DC 20231, or facsimile transmitted to the U.S. Patent and Trademark 00000100 09843820

08/20/2002 CCHAU1

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SATO

Attorney Docket No: 108397=00042

Serial Number: 09/843,820

Filed: April 30, 2001

Group Art Unit: 2816

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Examiner: A. Luu

SEMICONDUCTOR INTEGRATED CIRCUIT

PRELIMINARY AMENDMENT WITH FILING OF RCE

Commissioner for Patents Washington, D.C. 20231

August 19, 2002

Sir:

Prior to continued examination of the above-identified patent application, please amend the application as follows:

IN THE CLAIMS:

Please add claims 10-13 as follows:

(New)The semiconductor integrated circuit according to claims 3, wherein said power-on resetting circuit and said voltage generator include transistors formed through a same manufacturing process.

- (New) The semiconductor integrated circuit according to claim 10, wherein 11. said transistors are nMOS transistors that have same threshold voltages.
- (New) The semiconductor integrated circuit according to claims 7, wherein 12. said power-on resetting circuit and said voltage generator include transistors formed through a same manufacturing process.
- (New) The semiconductor integrated circuit according to claim 12, wherein 13. said transistors are nMOS transistors that have same threshold voltages.--